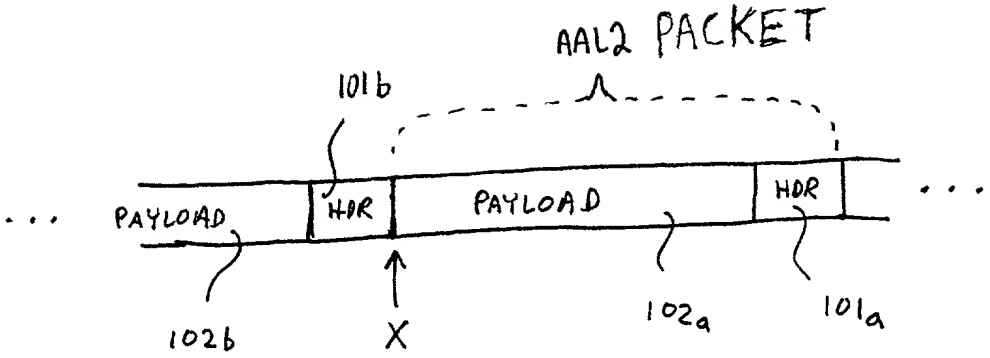



FIGURE 1A

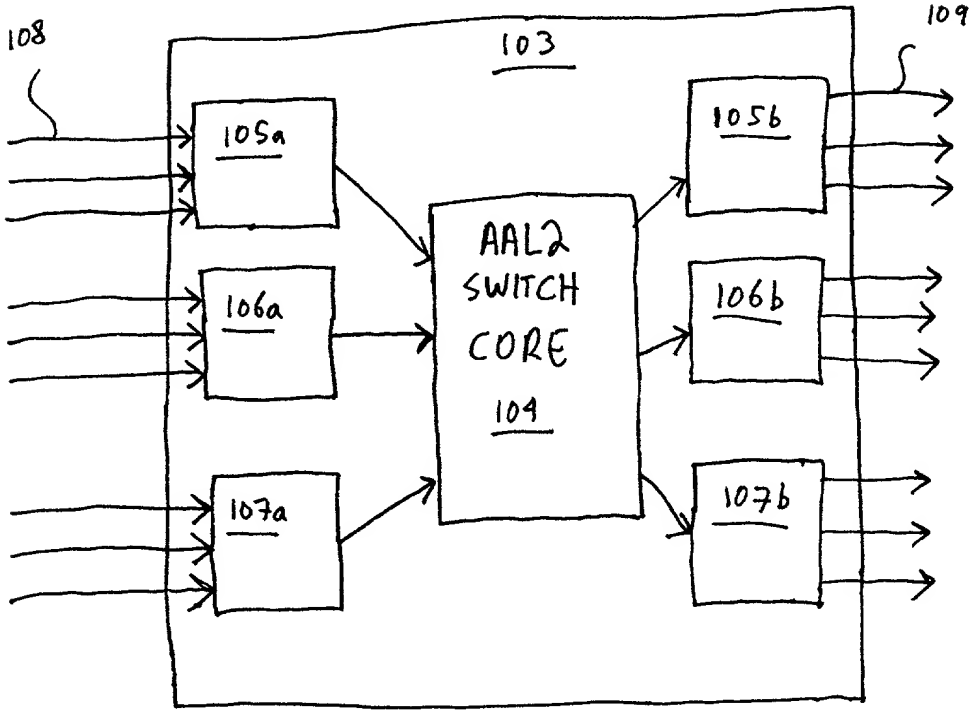



FIGURE 1B

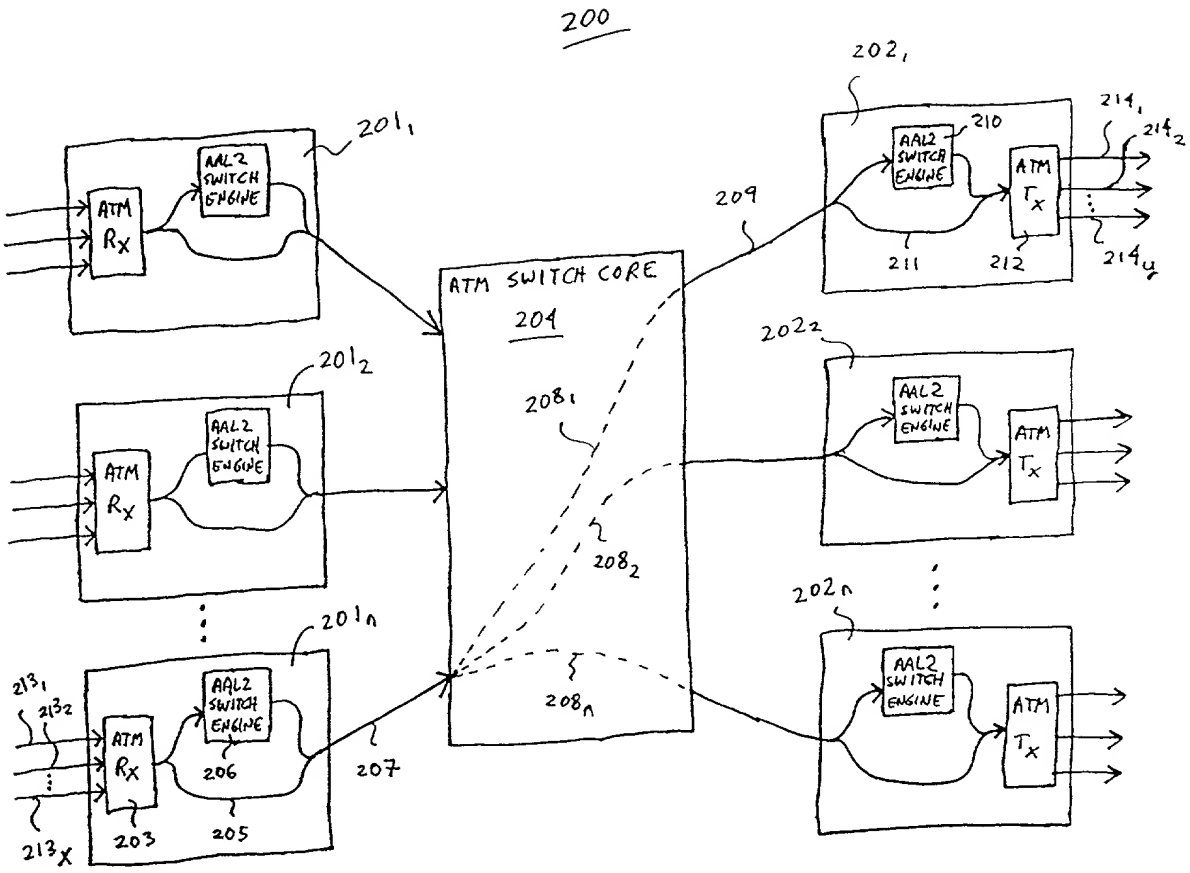

200
ATM SWITCH CORE
204
AAL2 SWITCH ENGINE
ATM Rx
ATM Tx
201 1
201 2
201 n
202 1
202 2
202 n
203
205
206
207
208 1
208 2
208 n
209
210
211
212
213 1
213 2
213 X
214 1
214 2
214 y

FIGURE 2

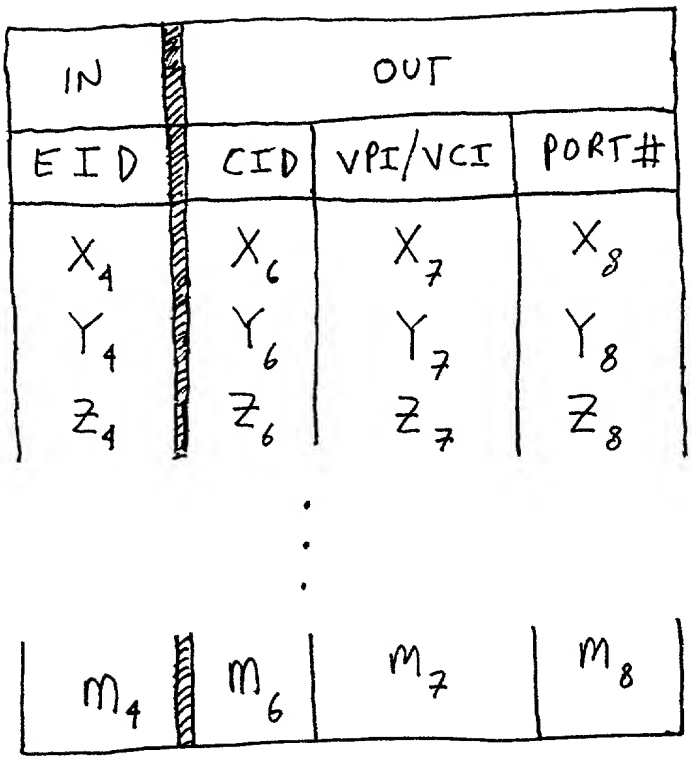
| IN | | | OUT | |
|---|---|---|---|---|
| PORT # | VPI/VCI | CID | EID | P# |
| $A_1$ | $A_2$ | $A_3$ | $A_4$ | $A_5$ |
| $B_1$ | $B_2$ | $B_3$ | $B_4$ | $B_5$ |
| $C_1$ | $C_2$ | $C_3$ | $C_4$ | $C_5$ |
| $q_1$ | $q_2$ | $q_3$ | $q_4$ | $q_5$ |

300

FIGURE 3

Patent Application Publication

Docket No. 50277-0005




400

FIGURE 4

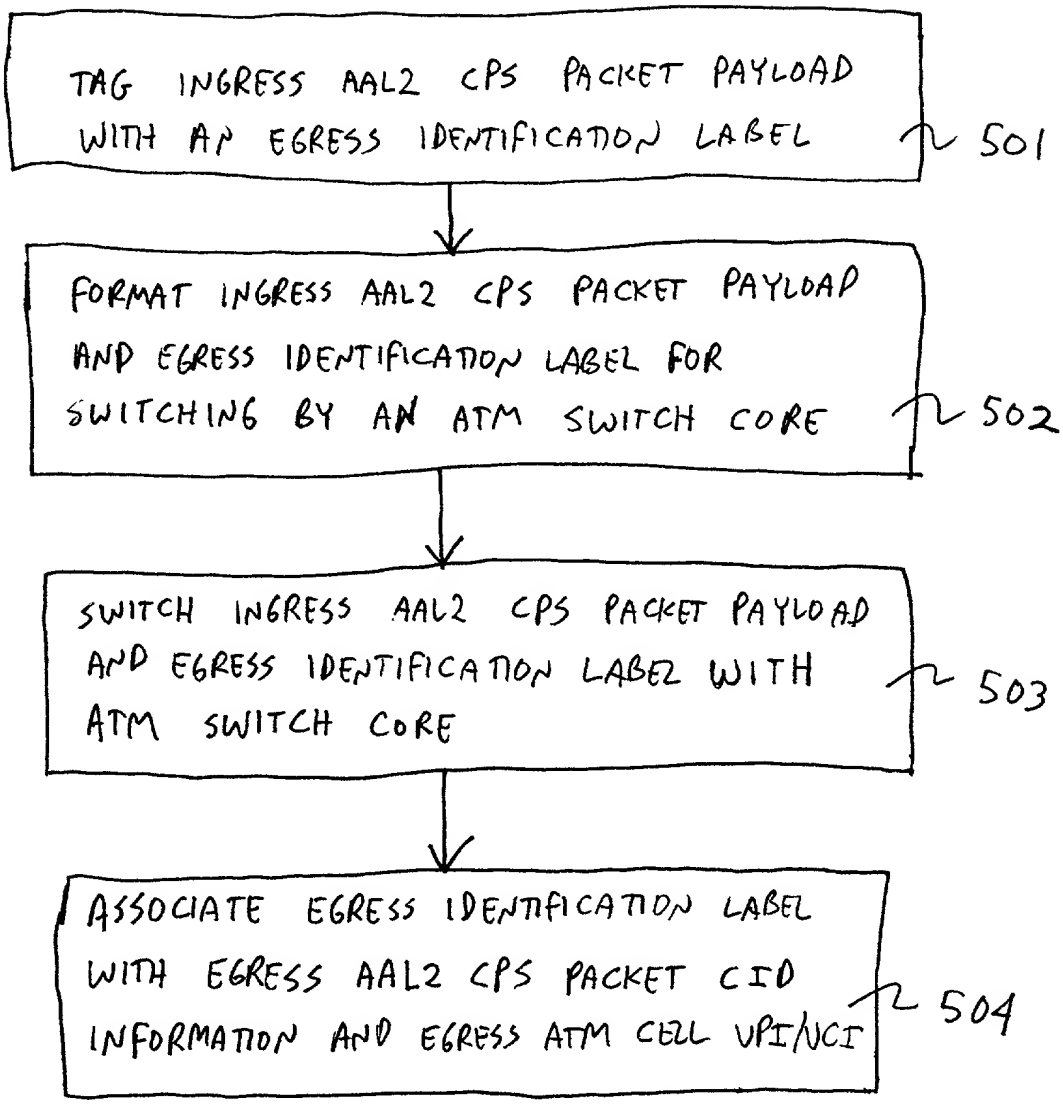

TAG INGRESS AAL2 CPS PACKET PAYLOAD WITH AN EGRESS IDENTIFICATION LABEL
501
FORMAT INGRESS AAL2 CPS PACKET PAYLOAD AND EGRESS IDENTIFICATION LABEL FOR SWITCHING BY AN ATM SWITCH CORE
502
SWITCH INGRESS AAL2 CPS PACKET PAYLOAD AND EGRESS IDENTIFICATION LABEL WITH ATM SWITCH CORE
503
ASSOCIATE EGRESS IDENTIFICATION LABEL WITH EGRESS AAL2 CPS PACKET CID INFORMATION AND EGRESS ATM CELL VPI/VCI
504

FIGURE 5